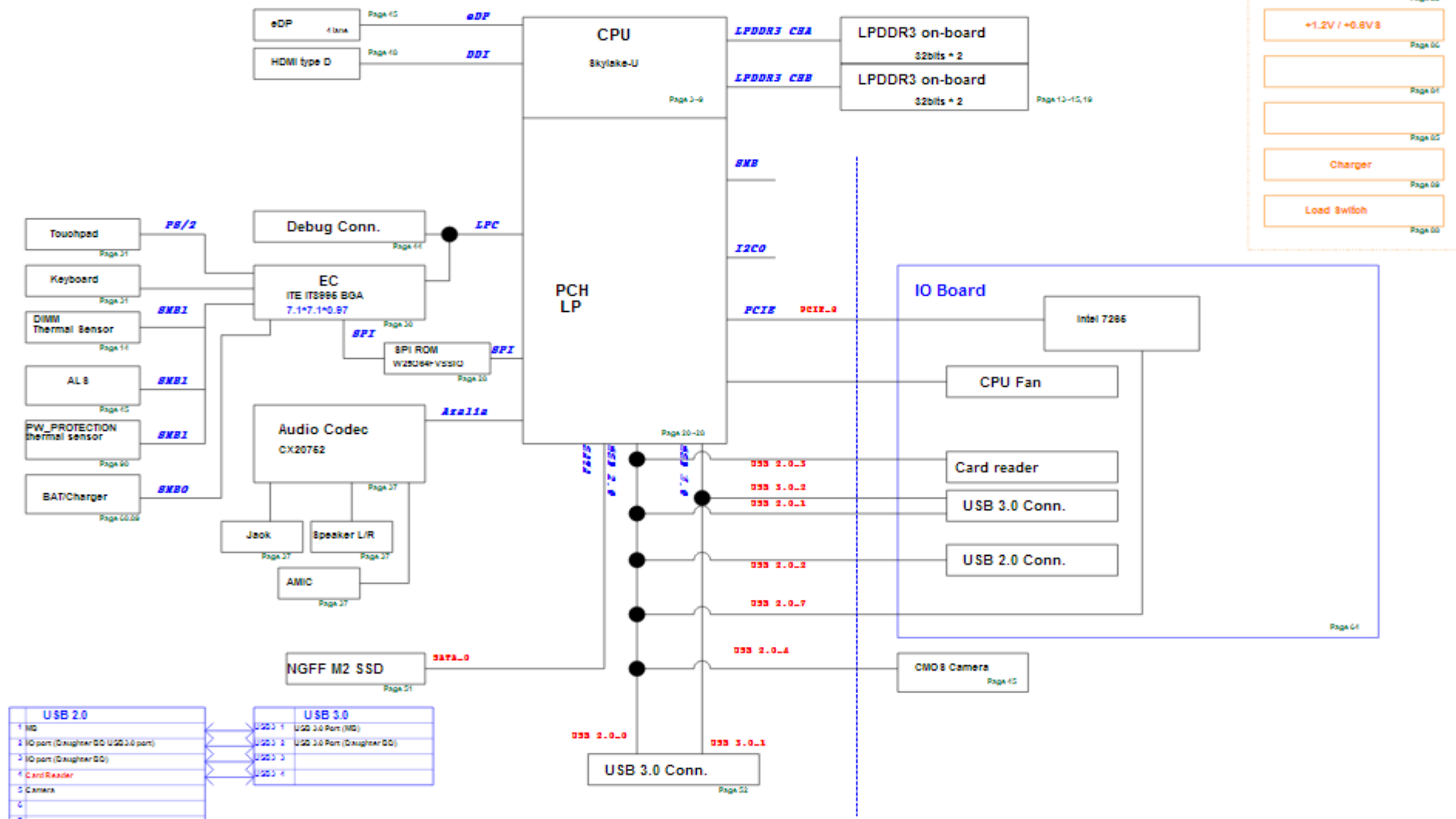


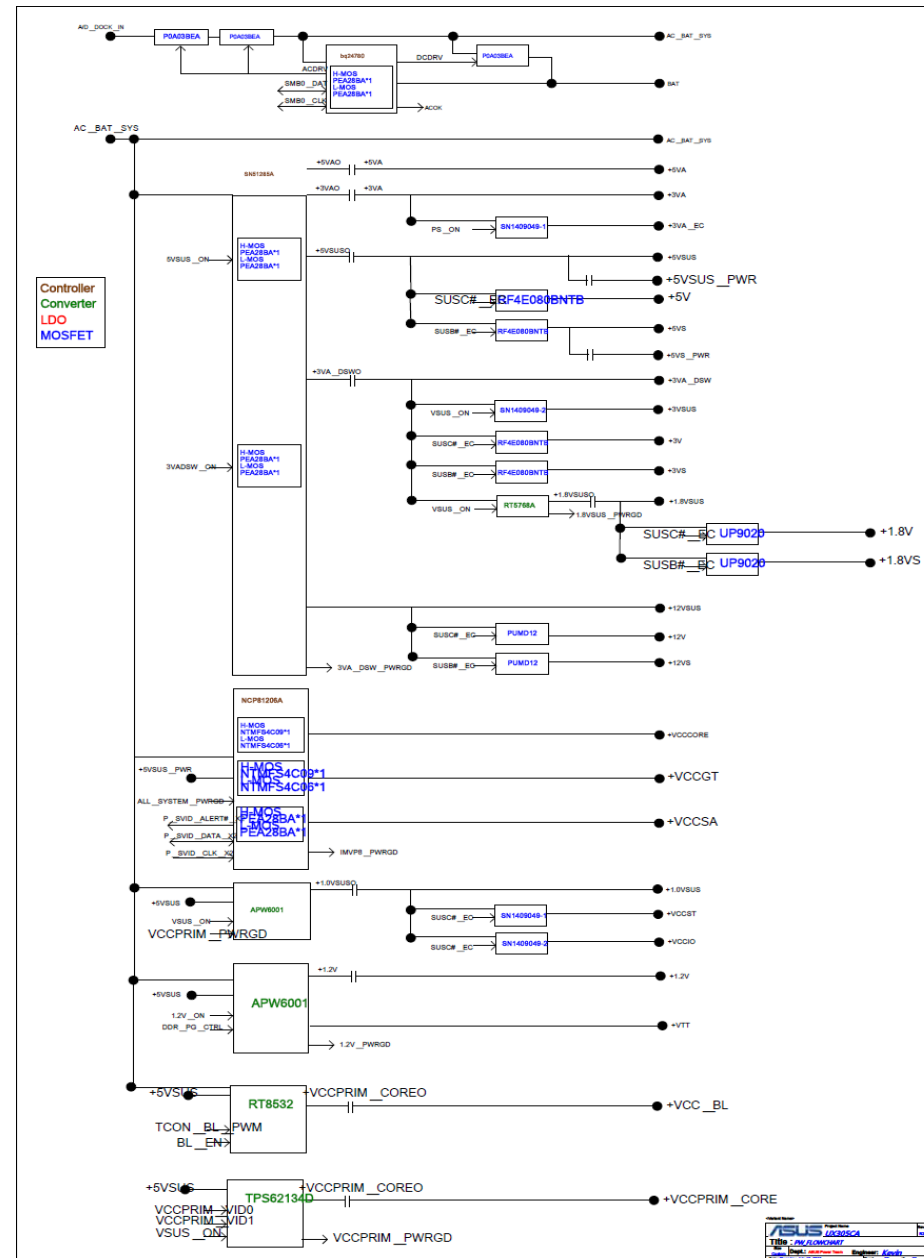
## UX305UA SCHEMATIC Revision 1.1

### BLOCK DIAGRAM

### Non Connected Standby

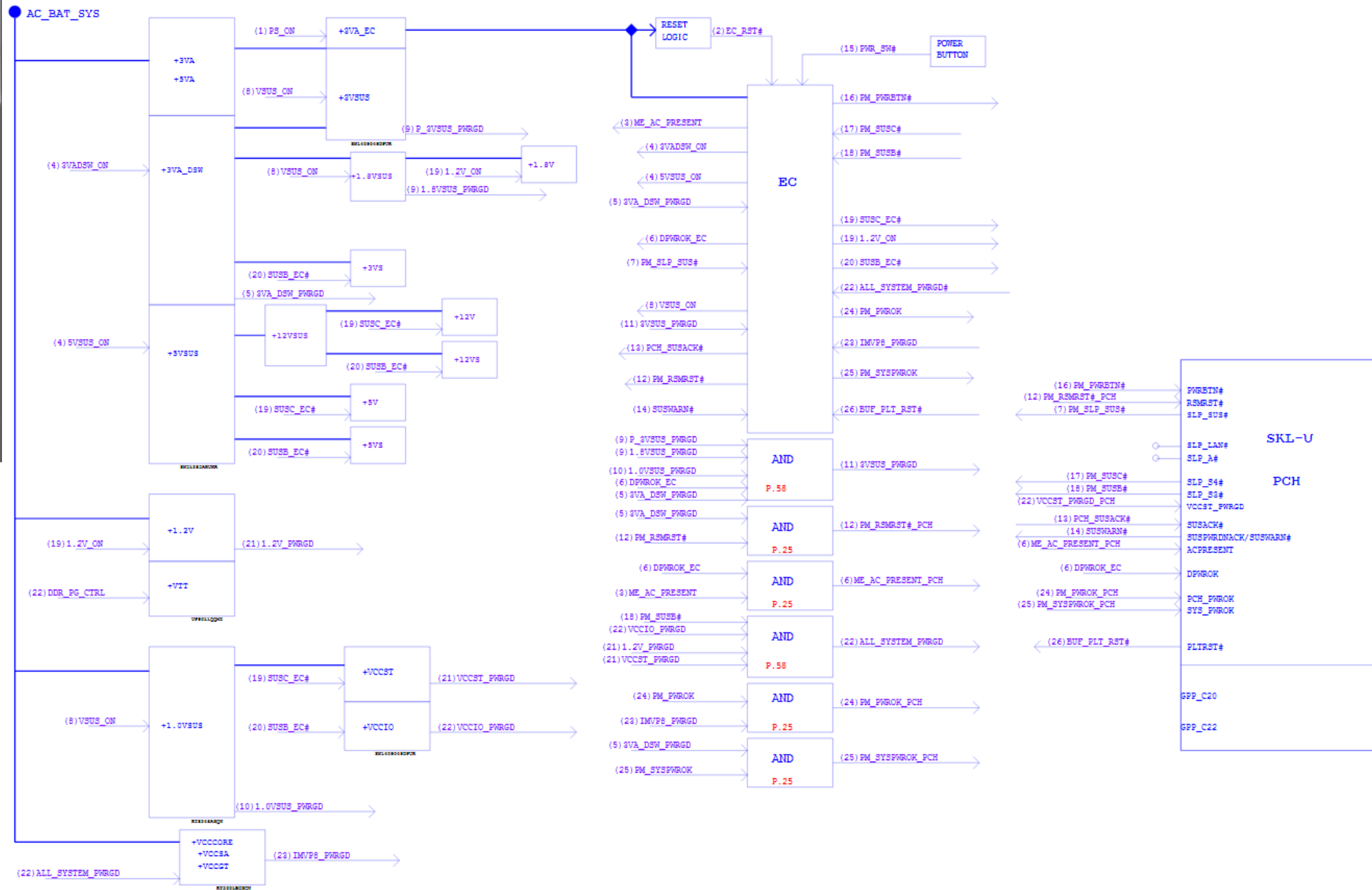


## POWER FLOW



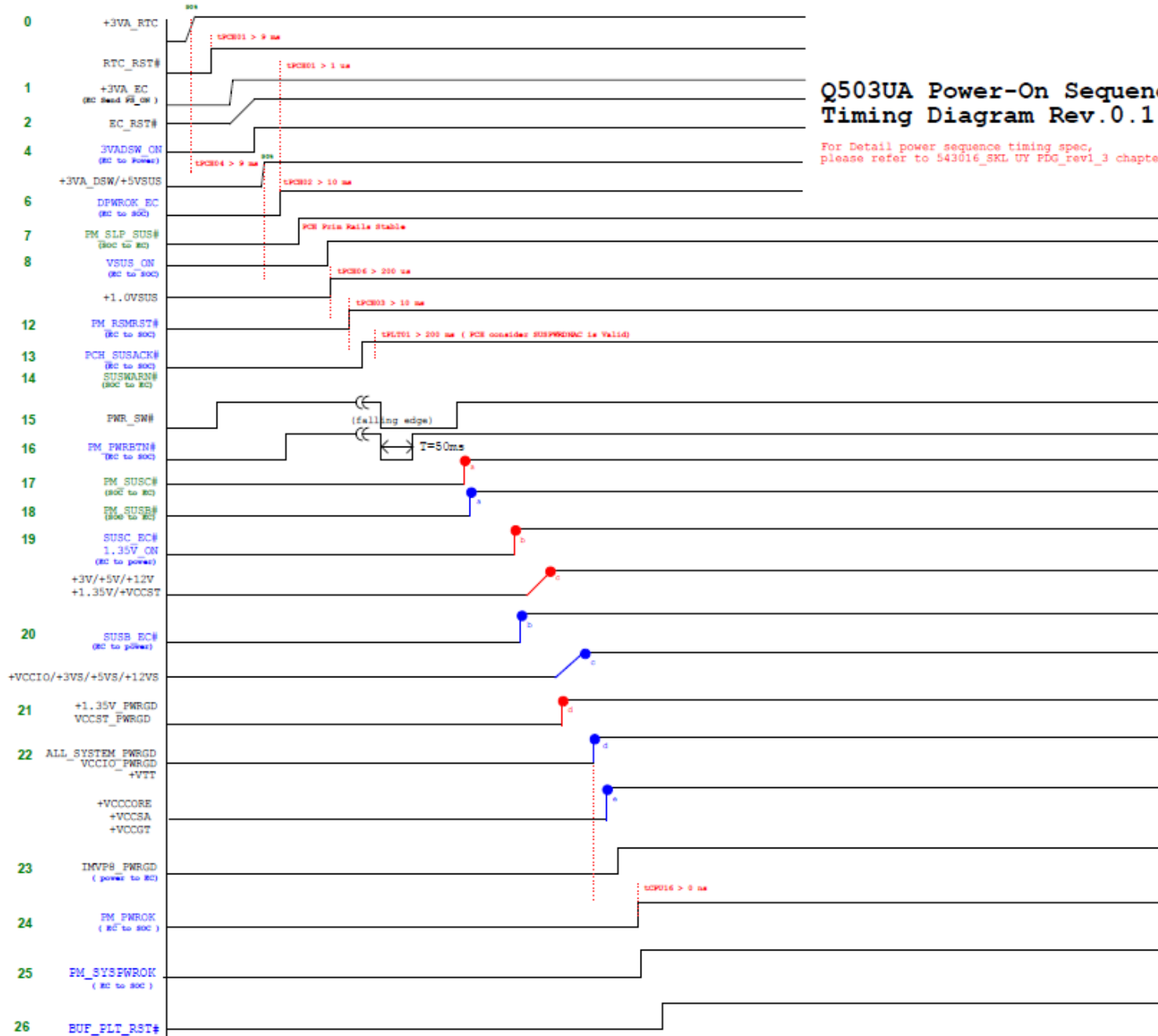
# POWER ON SEQUENCE

AC-IN Mode



# AC POWER ON SEQUENCE

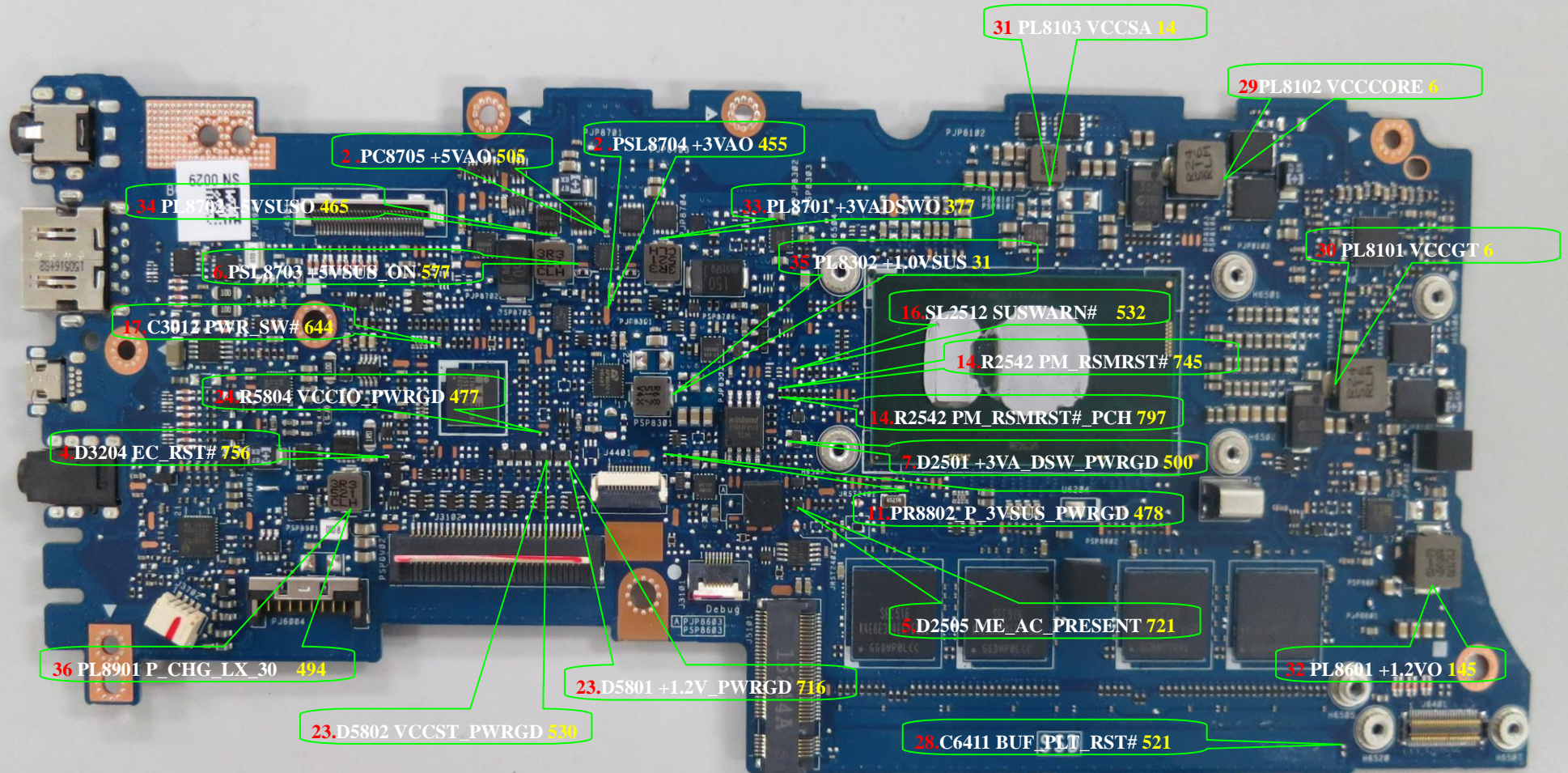
AC-IN Mode



## Q503UA Power-On Sequence Timing Diagram Rev.0.1

For Detail power sequence timing spec, please refer to 543016\_SKL UY PDG\_rev1\_3 chapter 43

## Signal Measure Point-Bottom





## Signal Measure Point-Top

